

Amendments to the Specification:

Please replace the last paragraph beginning on page 3 with the following amended paragraph:

In general terms the present invention is a system for passing clear DVD (digital video disk) program streams from a CPU (central processing unit) to an ~~[[MPEG-2]]~~ MPEG type decoder. In the system the CPU is connected to a first bus interface. A system memory is connected to the first bus interface via a memory bus. A second bus interface is connected to the first bus interface via a PCI (peripheral component interconnect) bus and a DVD data source is connected to the second bus interface. A packet data decoder is connected to the memory bus via a buffer. The CPU reads DVD data from the DVD data source across the PCI bus, decrypts the DVD data and creates ~~[[a]]~~ packet data, and sends the packet data to the buffer via the memory bus. The ~~[[MPEG-2]]~~ MPEG type decoder receives the packet data, via the transport bus from the buffer. In more general terms, the present invention is a novel way of connecting two existing busses in a computer or set-top box.

Please replace the first full paragraph on page 5 with the following amended paragraph:

The present invention is depicted as a block diagram in FIG. 1 and as a flow chart in FIG. 2. The present invention has general applicability, but is most advantageously used in a computer or set-top box. Inventively, in the computer or set-top box the system memory bus is connected to the transport stream input of the ~~MPEG-2 decoder~~ MPEG type decoder (e.g. MPEG-2). The CPU writes program streams directly from the memory bus to the MPEG-2 decoder, thus avoiding the PCI bus, and thus requiring less computations and greater efficiency. Generally, some type of buffering is required to de-couple the CPU from the MPEG-2 decoder. The buffering is preferably FIFO (First In, First Out) element located either externally to the MPEG-2 decoder, or located in the MPEG-2 decoder's frame buffer.